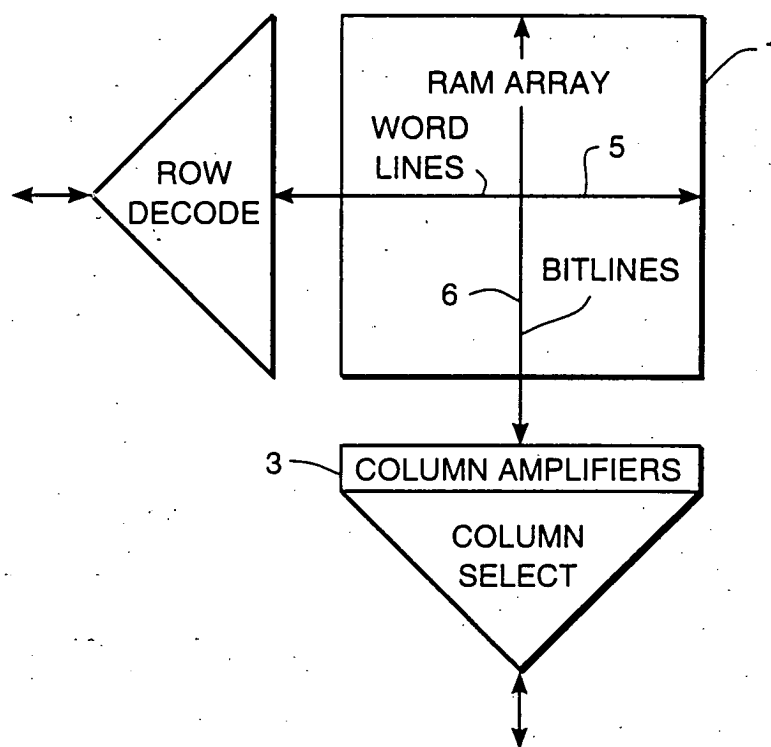
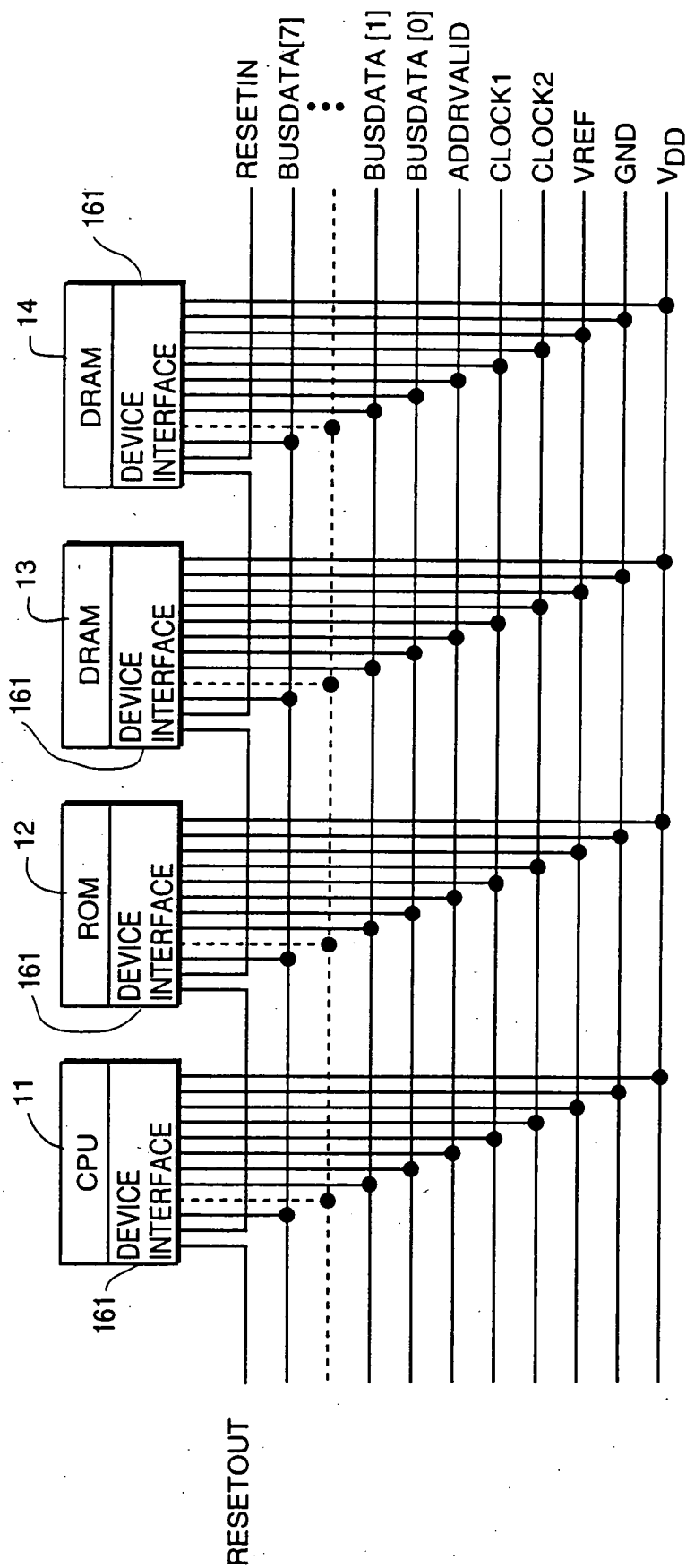
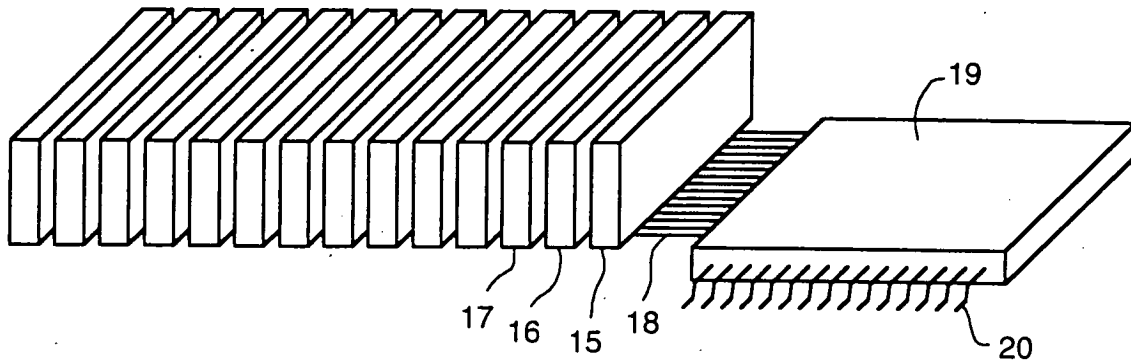


**FIG 1**

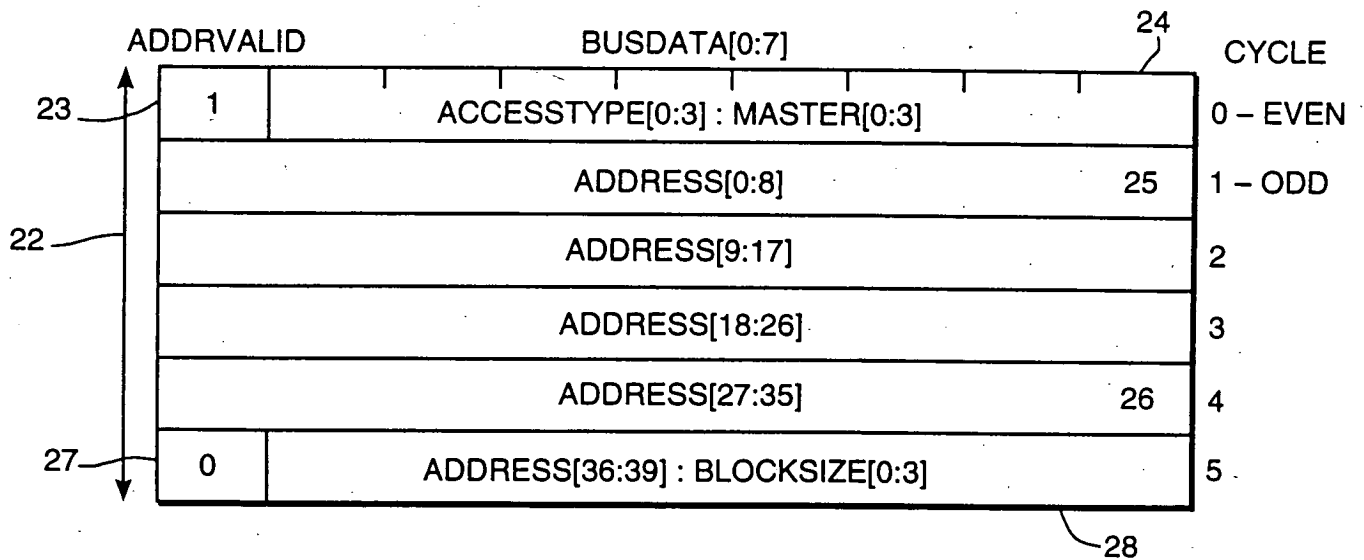
**FIG. 2**



# FIG 3

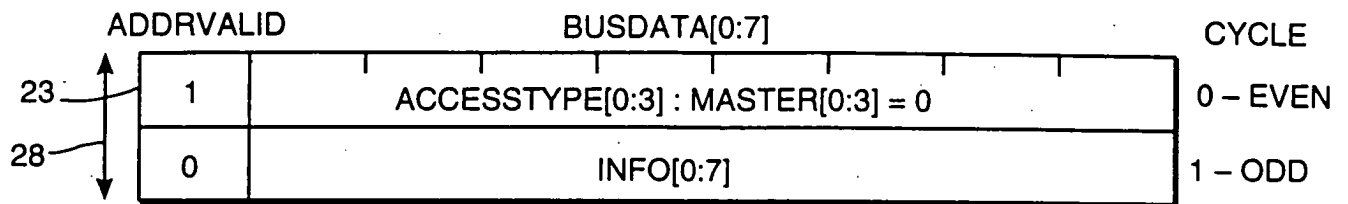


## REGULAR ACCESS

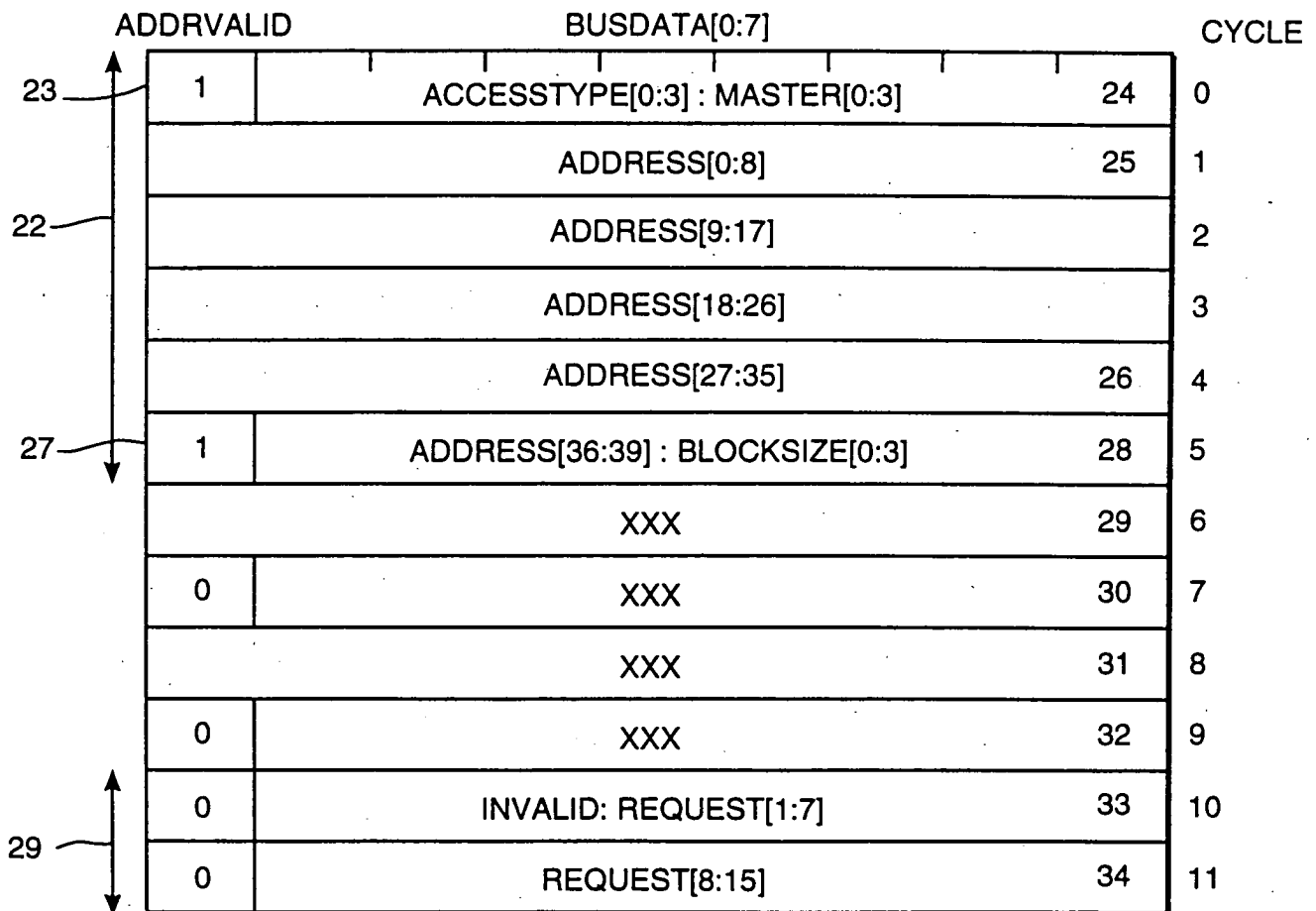


# FIG 4

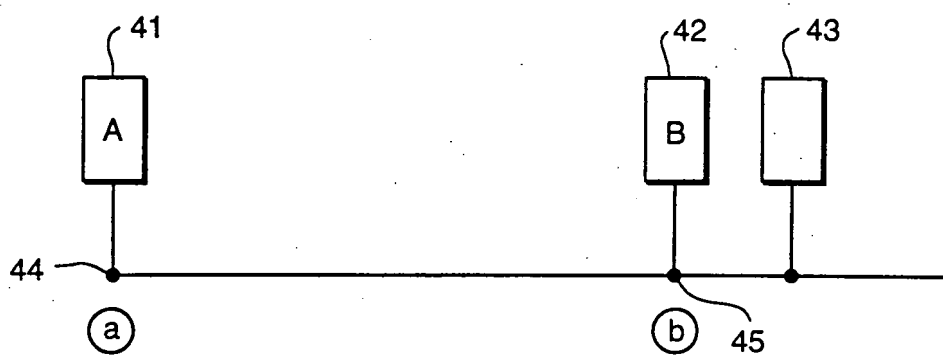
# REJECT (NACK) CONTROL PACKET



**FIG 5**

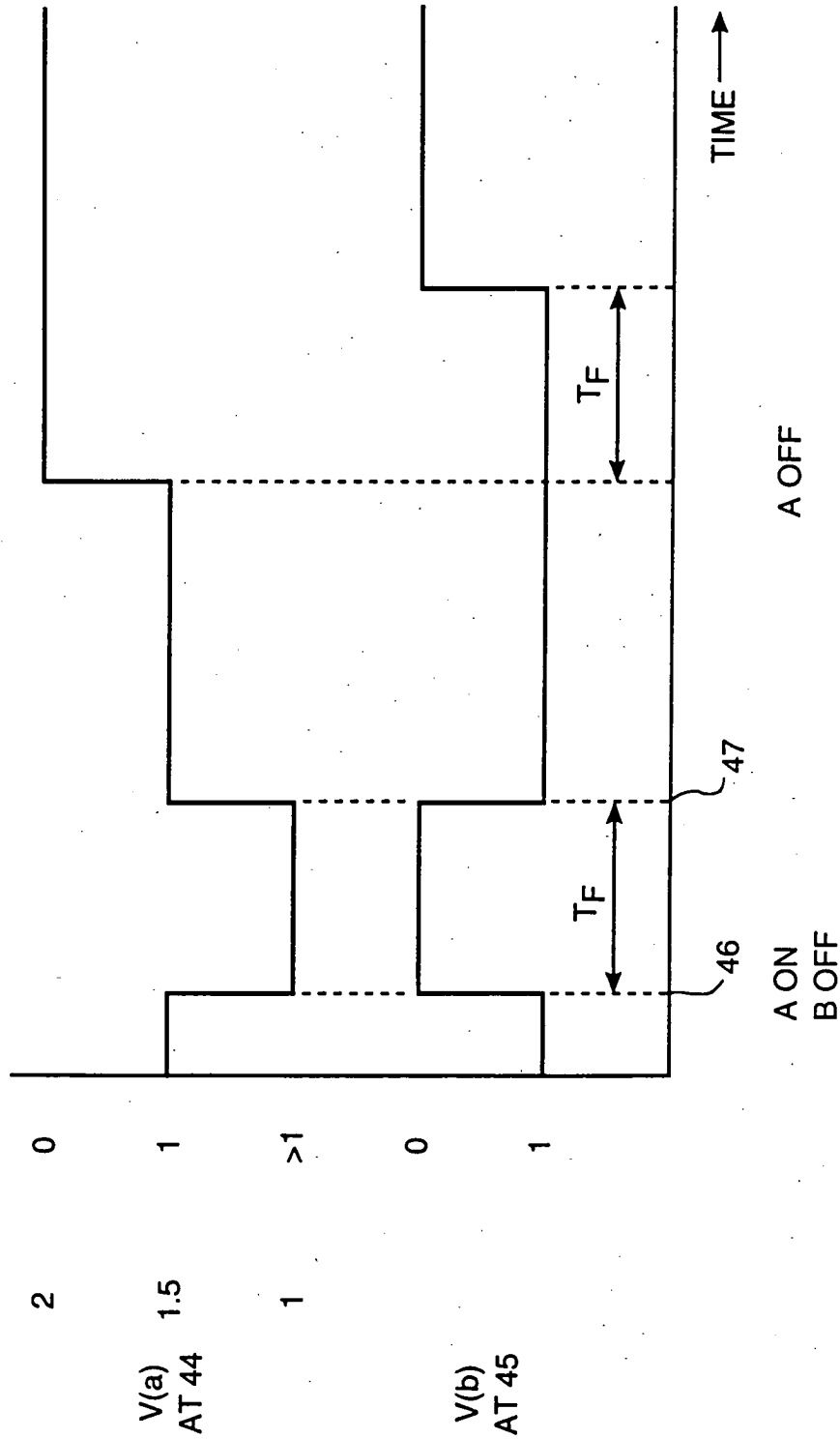


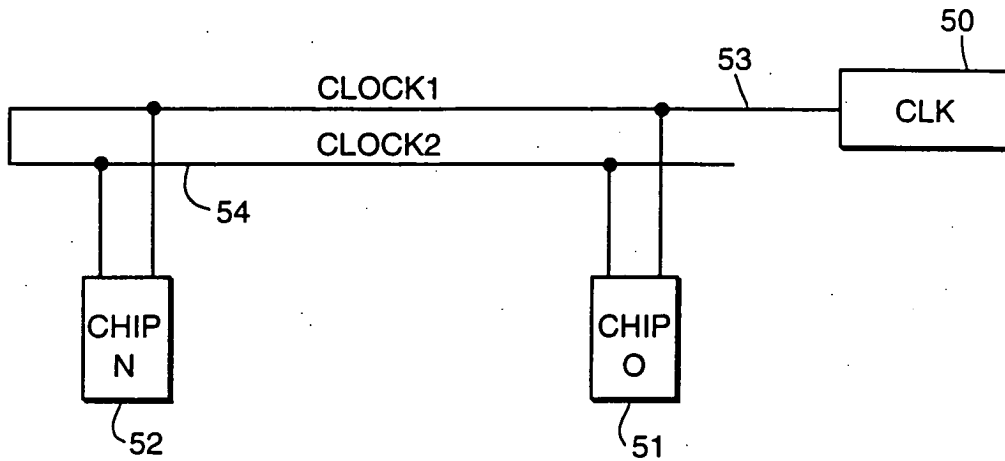
**FIG 6**



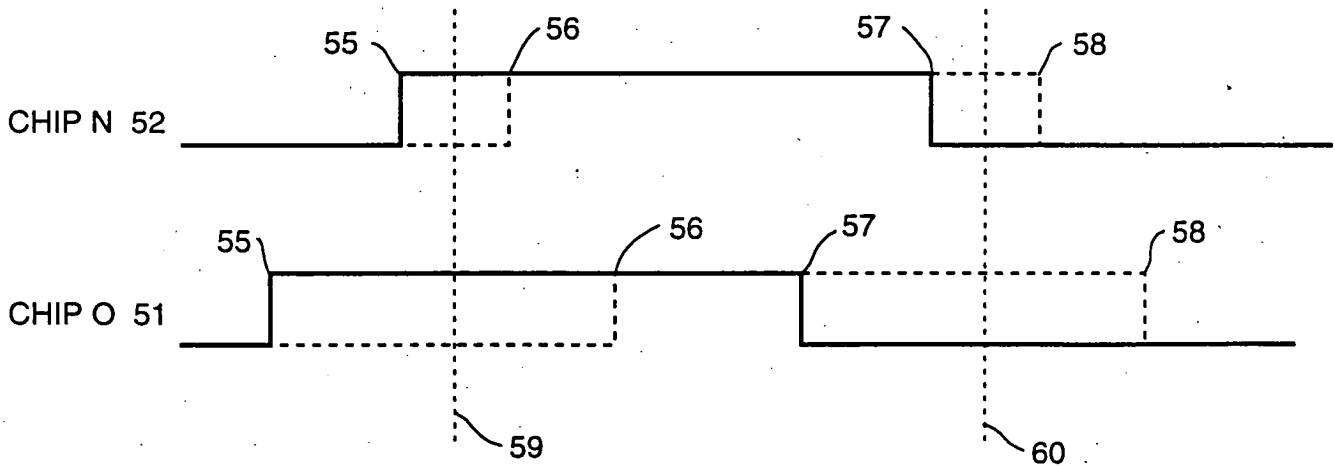
**FIG\_7A**

VOLTAGE LOGICAL  
VALUE



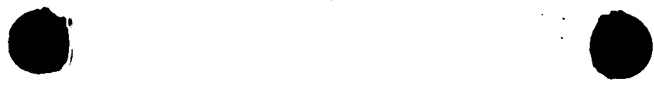


**FIG 8A**



**FIG 8B**

# FEEL







**FIG. 11**

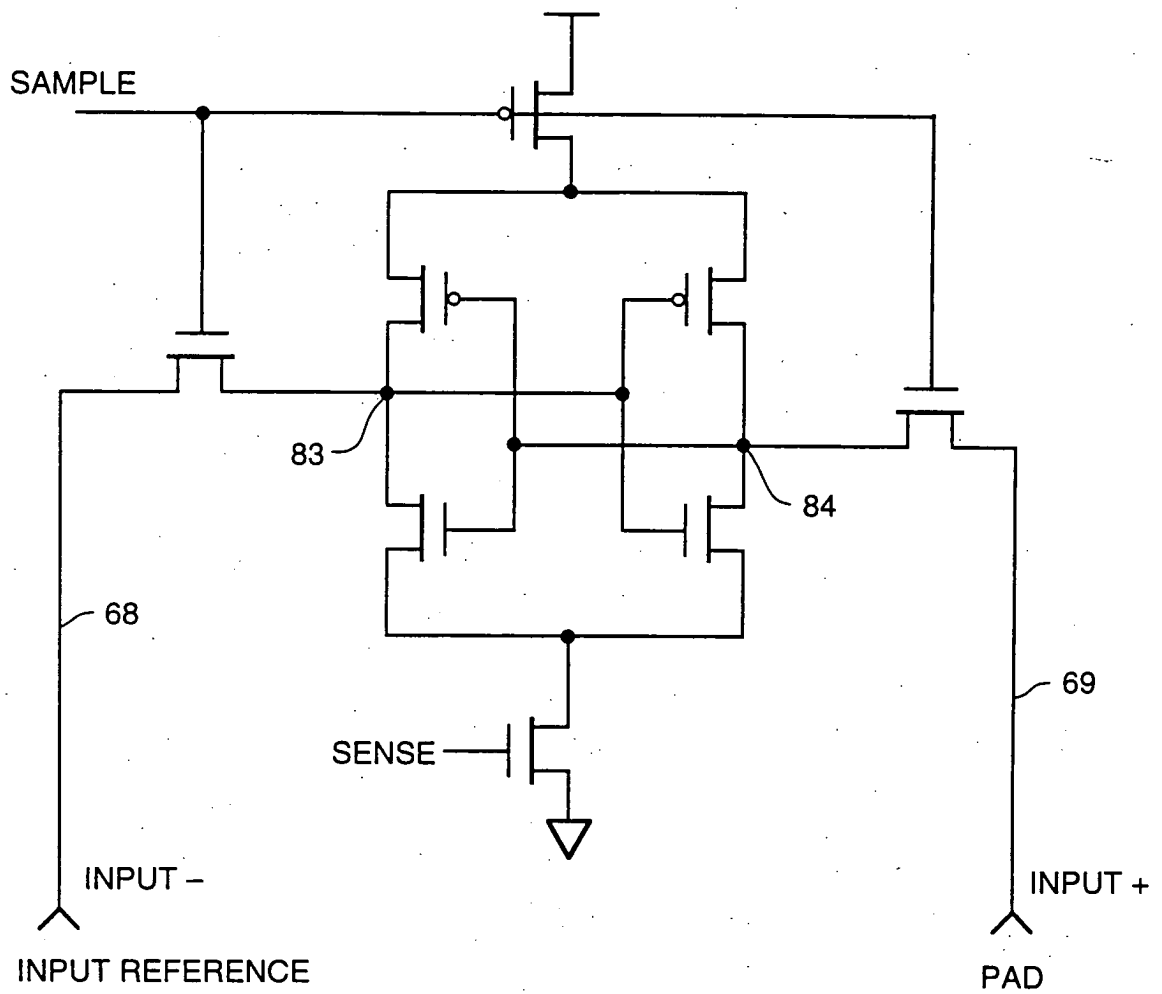
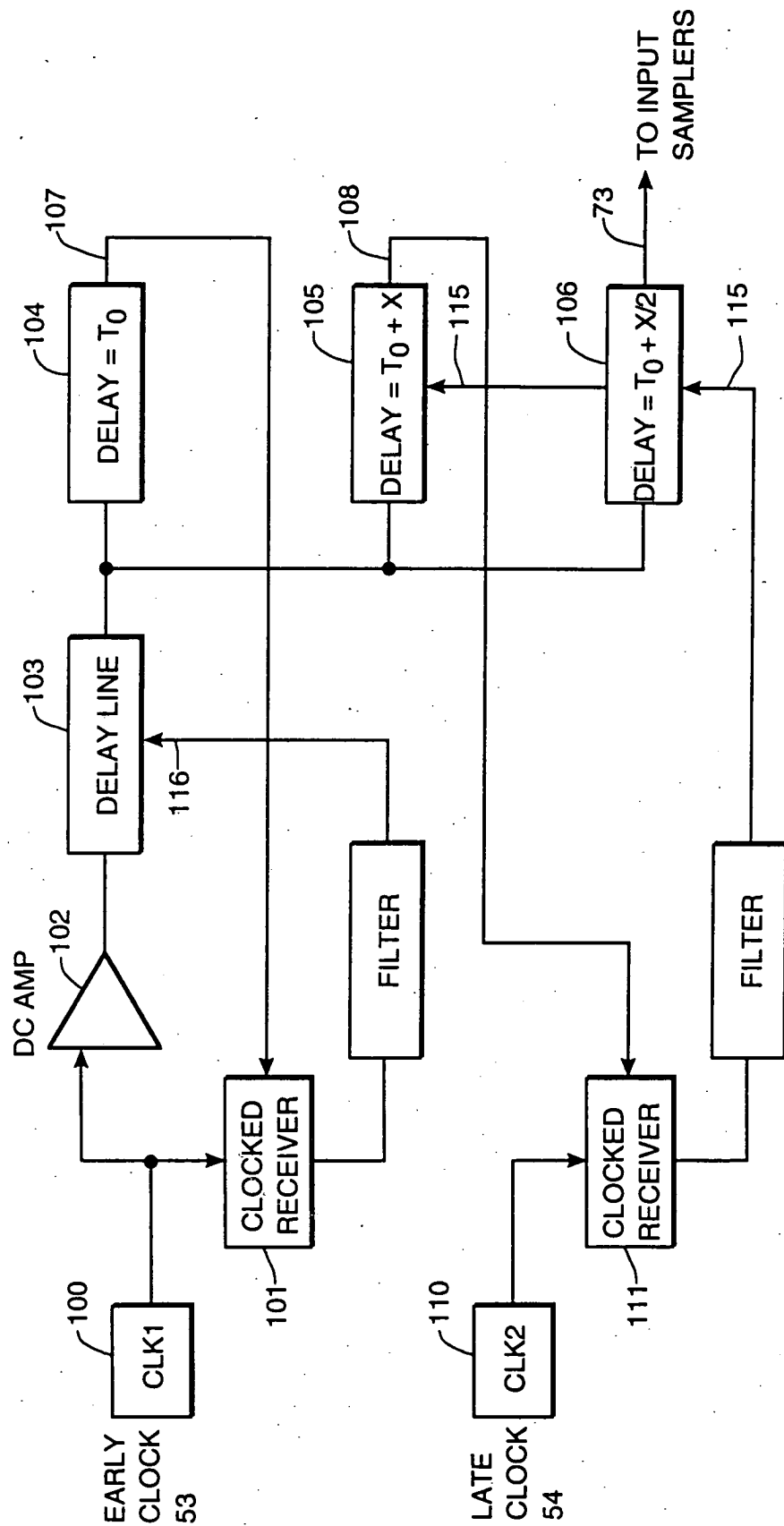
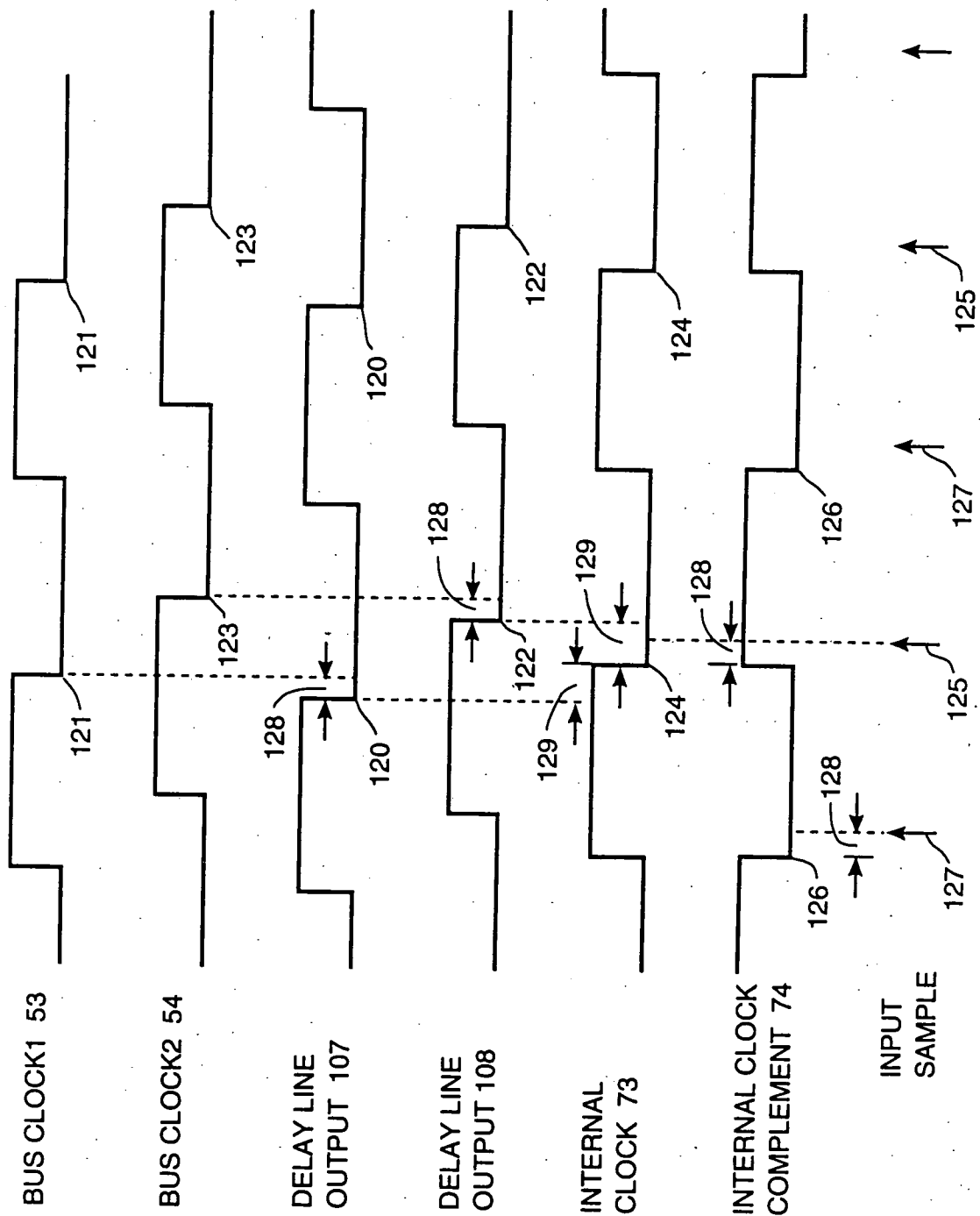
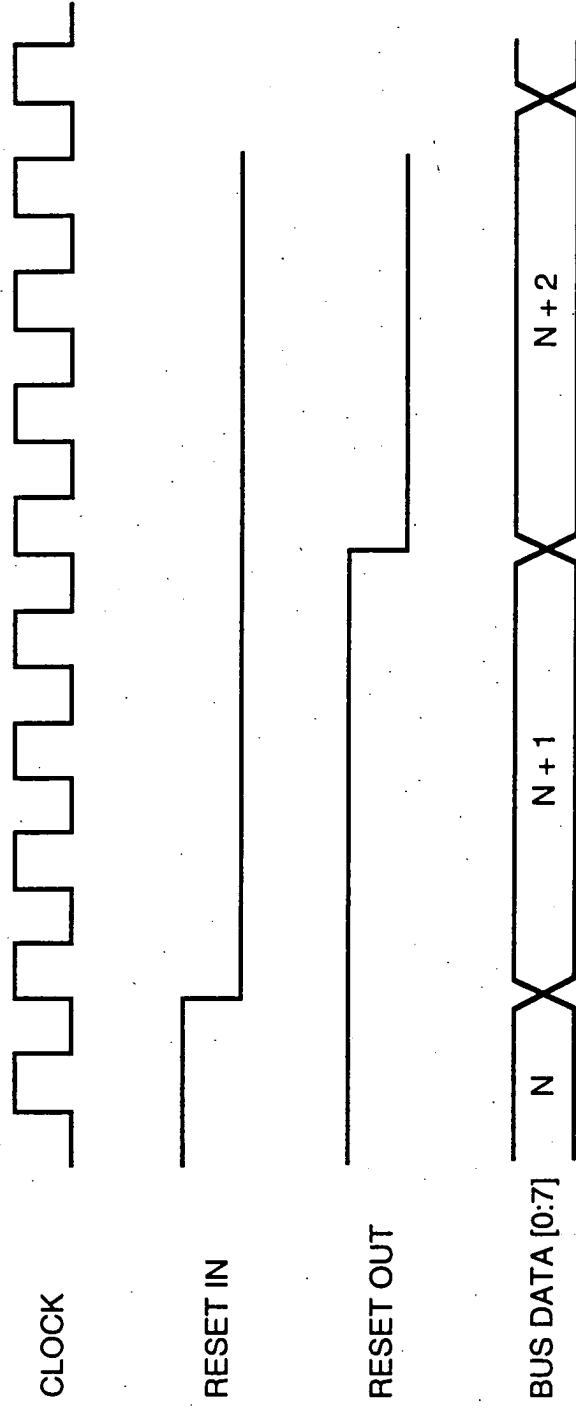


FIG. 12







**FIG. 15**

